

ANNIE1-electronics-design - Task #8809

Interface for PSEC electronics with ART system

05/15/2015 09:41 AM - Matthew Wetstein

Status:	Assigned	Start date:	07/15/2015
Priority:	Normal	Due date:	08/19/2015
Assignee:	Karen Hayrapetyan	% Done:	50%
Category:		Estimated time:	0.00 hour
Target version:		Spent time:	0.00 hour
Description (Karen)			

History

#1 - 10/09/2015 01:06 PM - Jonathan Eisch

- Status changed from New to Assigned
- Assignee set to Karen Hayrapetyan
- % Done changed from 0 to 50

Fragment generator for psec4 eval card exists.